

Analyze

Interfaces between embedded processors, memory, and logic functions on a chip or between devices on a circuit board are often the source of difficult-to-locate timing violations, especially for high-speed designs.

TimingDesigner's robust timing engine uses a timing diagram specification to accurately analyze parameters and identify violations that may otherwise go undetected until late in the design process. The ability to quickly evaluate design alternatives and compute worst-case timing margins makes TimingDesigner an excellent choice to help develop solutions for specific problem areas.

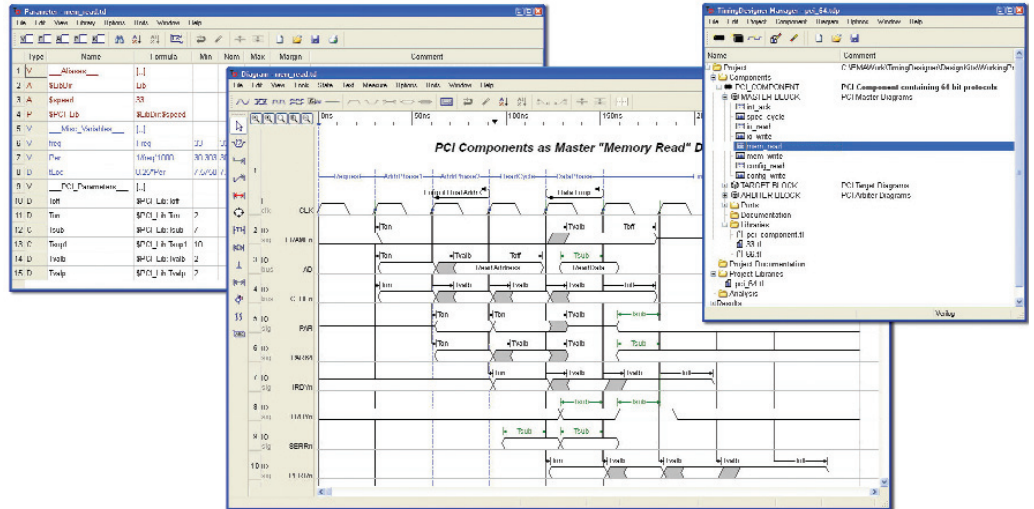
Document

TimingDesigner delivers the ability to clearly and accurately communicate design details by exporting or linking timing diagram files generated during the design process through OLE support or in its native format. The standard, easy-to-interpret format of timing diagram specifications improves the communication of complex design information.

Specify

Evaluating alternatives is key to developing specifications that can accurately convey design details and timing budgets. TimingDesigner supports the early investigation of timing options and provides a straightforward means to clearly specify the sequence of events and timing relationships required for modules or subsystems to communicate as expected.

TimingDesigner 9.0 offers Design Kits for processors from companies such as Intel®, Freescale™, Analog Devices and Texas Instruments. Kits are available for memory styles such as DDR, DDR2, QDR, and QDR2; Flash, SRAM, and DRAM. Interface specifications include PCI (33 and 66 MHz) PCI-Express and USB.



TimingDesigner Design Kit features

- Exhaustive list of components kits – Designers can choose from a comprehensive list of today's most popular design components, interface specifications, and frequently requested parts.
- Quick assembly of a timing project – Complete "importation" into the currently opened timing project allows users to get to the business of timing analysis quickly.
- Extensive Parameterization – Parameterized diagrams for easy adjustment of clock frequencies, configuration modes, speed ratings, and voltage varieties.
- Includes all necessary documentation – Contains manufacturer's data sheet from which it was built, and other documents where applicable.

Design Kit components

Processors from:

- Intel
- Freescale
- Analog Devices
- Texas Instruments

Various memory styles from leading manufacturers include:

- DDR, DDR2, QDR, QDR2
- Flash
- SRAM
- DRAM

Interface specifications include:

- PCI (33 and 66 MHz)
- PCI-Express
- USB

Call Us Today!

For additional information, visit us at www.timingdesigner.com or call 877.362.3321.

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