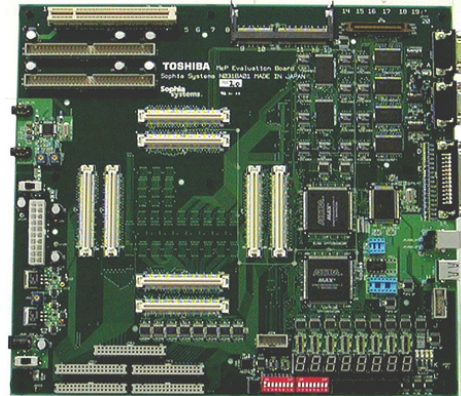


## MeP Core Evaluation Board



**[BDS006 EVA BOARD/MeP CORE]**



**[BDS004 MAIN]**

BDS004 and BDS006 are the evaluation boards developed for Toshiba's configurable processor MeP.

They can be used for software development or performance confirmation before you begin to create a prototype.

### [BDS006(EVA BOARD/MeP CORE) Specifications]

- "BDS006" is used in combination with "BDS004".
- FPGA(EP1S40F1020C7) with MeP core design data
- SDRAM (4 X 2M X 16 bit) X 2
- OSC 20MHz
- Serial I/F 2 ch
- 8-bit DIP switch 4 pieces
- Push switch 4 pieces
- LED 13 pieces
- JTAG connector for use with Sophia's emulator
- ATX connector for AC
- Dimension 150mm X 200mm

### [BDS006 Configuration]

- Evaluation Board/MeP CORE Board
- CD-ROM includes:
  - MeP core evaluation board schematic(PDF)
  - MeP core evaluation board parts list(PDF)
  - Hardware user's manual
  - MeP core data(FPGA)

The use of the board requires the following:

- Development tool for RedHat/MeP
- Altera/PLD tool(Quartus II)
- UniSTAC II/J for MeP emulator
- AC adapter(VK0014 by Sophia) or ATX AC adapter for PC

### [BDS004(MeP EVA BOARD/MAIN) Specifications]

- Flash Memory(2M Bytes), SRAM (2M Bytes)
- Serial I/F (2 ch), Parallel (1 ch)
- Sound
- 7-segment LED 8 pieces
- 8-bit DIP switch 2 pieces
- Push switch 6 pieces
- OSC 20MHz
- Dimension 300mm X 270mm

### [BDS004 Configuration]

- CD-ROM includes:
  - MeP evaluation board schematic(PDF)
  - MeP evaluation board parts list(PDF)
  - Hardware user's manual
  - PLD data

All configurations are subject to change without notice.

Visit <http://www.MePcore.com> for Toshiba's configurable processor MeP

*Sophia Systems Co., Ltd.*

URL: <http://www.sophia.com>

Email: [sales@sophia.com](mailto:sales@sophia.com)

### Order Information

BDS004	MeP EVA BOARD/MAIN
BDS006	EVA BOARD/MeP CORE
VK0014	US AC Adapter(Optional)