

## UniSTAC™ II/J for VR4122/31/81A



- Supports NEC VR4122/4131/4133/4181A
- Supports CPU maximum clock speed
- N-Wire (Debug Port) interface
- 4 hardware breakpoints
- Unlimited software breakpoints
- Download to Flash Memory capability
- Watchpoint®, Sophia's powerful high level language debugger, is included with all Sophia Systems emulators (USB: Windows98/Me/2000/XP platforms) (TCP/IP: Windows98/Me/NT/2000/XP platforms)
- USB interface for high-speed communication with host computer

\*Note: MIPS 16-bit instruction and 64-bit address area is

### Specifications

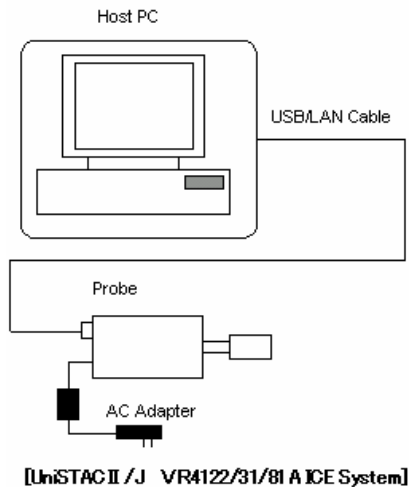
<b>Supported CPU</b>	VR4122(μPD30122), VR4131(μPD30131), VR4133, VR41814A(μPD30181A)
<b>CPU Clock</b>	Supports the CPU's maximum clock speed
<b>Target Vcc</b>	VCCIO = 3.3V
<b>Memory</b>	All memory is available to user except that debug interrupt vector address: H' FFFF FFFF BFC0 1000 ~ H' FFFF FFFF BFC0 101F is not available.
<b>Interrupts</b>	Both internal and external interrupts are available to user during real-time execution
<b>Reset</b>	Reset the CPU during program execution
<b>Breakpoints</b>	<ul style="list-style-type: none"> <li>• Hardware breakpoints(2 address break, 2 access break, memory read/write/access)</li> <li>• Unlimited Software breakpoints</li> <li>• Other break options: Forced break from debug monitor</li> </ul>
<b>Set Up</b>	The following target signals can be masked with software. RESET, NMI, INT
<b>Download to External FLASH Memory</b>	Download to external Flash ROM capability Support over 100 different flash memory devices Write and re-write (clear, fill-up) capability

## Configuration

The UniSTAC™ II/J VR4122/31/81A provides a high-level of software debugging from your PC/AT or notebook computer. It is a development tool for NEC VR4122/31/33/81A. The UniSTAC™ II/J VR4122/31/81A ICE can be connected to a JTAG connector on the target.

### Hardware

The UniSTAC-II/JTAG VR4122/31/81A emulation system consists of UniSTAC-II/J\_Lite VR4122/31/81A JTAG Probe Set with USB/LAN host interface.



### Software

Watchpoint®, a high-level language debugger for Windows; is included with the VR4122/31/81A Probe Set.

### Media:

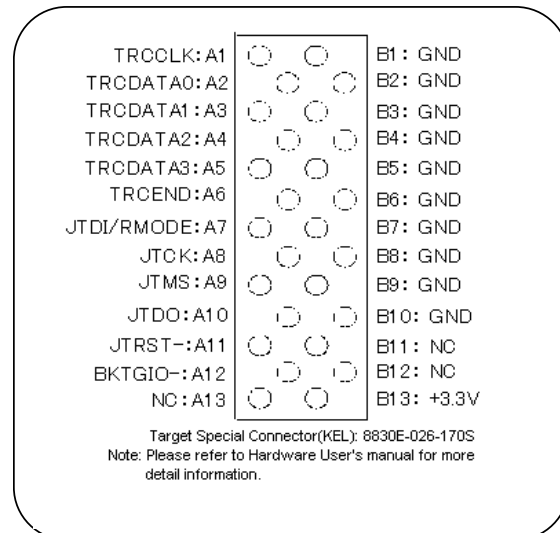
CD-ROM

### Supported Tool Chains

Compilers

- Green Hills GHS VR
- GNU

### Target Connection



NI-wire Connector Pin Assignment (Top View)

## Ordering Information

Part No.	Description
UT36001E	Probe set, UniSTAC-II/J VR4122/31/81A ICE with USB/LAN interface, and Watchpoint® debugger

### \* System Requirements

OS: <USB>Windows98/Me/2000/XP; <TCP/IP>Windows98/Me/NT/2000/XP

Memory: Minimum 32Mbytes (64 Mbytes recommended); Hard Disk: 20Mbytes for installation.

\* All configurations are subject to change without notice.

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\* UniSTAC™ is trademark of Sophia Systems Co. Ltd.

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